1. The internal RAM memory o f the 8051 is.
2. 32 Bytes
3. 64 Bytes
4. 128 Bytes
5. 256 Bytes
6. The 8051 has 16-bit counter/timers.
7. 1
8. 2
9. 3
10. 4
11. The 8051 can handle interrupt sources.
12. 3
13. 4
14. 5
15. 6
16. The special function registers are maintained in the next 128 location after the general purpose data storage and stack.
17. True
18. False
19. This statement will set the address of the bit to 1 (8051 Micro controller).

SETB 01h

A. True

B. False

1. MOV A,@R1 will
2. Copy R1 to the accumulator
3. Copy the accumulator to R1.
4. Copy the contents of memory whose address is in R1 to the accumulator
5. Copy the accumulator to the contents of memory whose address is in R1.
6. The following program will receive data from port 1, determine whether bit D0 is high, and then send the number FFH to port3.

READ: MOV A,P1

ANL A,#01H

CJNE A,#01H,READ

MOV P3,#0FFH

1. True B. False
2. When Pin No. 31 is connect to VCC and 8051 is reset, the program counter points to the first program instruction in the.
3. Internal code memory
4. External code memory
5. Internal data memory
6. External data memory
7. An alternate function of port pin P3.4 in the 8051 is.
8. Timer0
9. Timer1
10. Interrupt 0
11. Interrupt 1
12. The I/0 ports that are used as address and data for external memory are.
13. Ports 1 and 2
14. Ports 1 and 3
15. Ports 0 and 2
16. Ports 0 and 3
17. The total external data memory that can be interfaced to the 8051is.
18. 32K
19. 64k
20. 128K
21. 256k
22. Bit- addressable memory locations are.
23. 10H through 1FH
24. 20H through 2FH
25. 30H through 3FH
26. 40H through 4FH
27. The 8-bit address bus allows access to an address range of.
28. 0000 to FFFFH
29. 000 to FFFH
30. 00 to FFH
31. 0 to FH
32. What is the function of pin described ‘VPP’ at pin no. 31.
33. For external RAM excess.
34. For external ROM excess.
35. For internal ROM excess.
36. Used by programmer kit while programming chip.
37. What is the function of pin described ‘ PROG’ at pin no 30.
38. For external RAM excess.
39. For external ROM excess
40. For internal ROM excess.
41. Used by programmer kit while programming chip.
42. 8051 has.
43. 16-bit data bus
44. 14-bit data bus
45. 10- bit data bus
46. 8- bit data bus
47. 8051 has.
48. 16-bit address bus
49. 14-bit address bus
50. 10- bit address bus
51. 8- bit address bus
52. One ULN2003A IC can be used to dive.
53. Only 1 stepper motor at a time.
54. 2. Stepper motor at a time
55. 4 stepper motor at a time.
56. Microcontrollers often have.
57. CPU
58. RAM
59. ROM
60. All the above
61. The contents of the accumulator after this operation.

MOV A,#0BH

ANL A,#2CH

Will be

1. 11010111
2. 11011010
3. 00001000
4. 00101000
5. Which of the following instructions will move the contents of register 3 to the accumulator.
6. MOV A,@R3
7. MOV A,03H
8. MOV A,R3
9. Both B and C
10. Instruction MOVC A,@A+DPTR is used for ROM memory.
11. True B. False
12. By default all the Ports are.
13. Input Ports
14. Output Ports
15. Both A. and B.
16. For 8-bit auto reload in Timer0 count should be loaded to .
17. TH0 Reg.
18. TL0 Reg.
19. Both A. B.
20. TH1 REG.
21. For baud rate selection value is loaded into register
22. Timer 0
23. Timer 1
24. Both A. and B.
25. None of these.